

ABOUT KCG COLLEGE OF TECHNOLOGY

KCG College of Technology was founded in 1998 to fulfill the Founder-Chairman, Dr KCG Verghese's vision of "Making Every Man a Success and no Man a Failure". It is affiliated to Anna University, Chennai and approved by AICTE, New Delhi. The college offers eight UG programmes in Engineering and seven PG programmes of Master of Engineering in Power Electronics and Drives, Computer Science and Engineering, Engineering Design, VLSI Design, Manufacturing Engineering, Communication Systems and Master of Computer Application. The college has full-fledged infrastructure facilities in all departments. The Institution is known for strict academic discipline and standards and its main vision is to offer best engineering education and to develop the young minds into an all-round personality.

ABOUT THE DEPARTMENT OF ECE

The Department of Electronics and Communication Engineering was established in the year 1998. It is accredited by NBA and certified by ISO-2008. It offers 4 year B.E. Degree programme and two 2 year post-graduate programmes, Communication Systems and VLSI Design. The department runs to meet the ever challenging technical needs in many areas of electronics and communication engineering such as Electronics, Communication Systems, VLSI Design, Wireless and Mobile Communication, Image Processing, and Embedded Systems. The department has conducted many national-level seminars, conferences, workshops, short-term training programmes and faculty development programmes sponsored by various funding agencies. The infrastructure and lab facilities are upgraded from time-to-time and provide excellent opportunities for students and researchers to innovate. The department has distinguished faculty from renowned institutions in India. The Department understands the needs

of the Industry and strives to produce quality Engineers with Professional orientation.

OBJECTIVES OF THE PROGRAMME

The objective of the programme is to provide a comprehensive understanding of VLSI Design concepts. The main aim of the FDTP is to facilitate the teaching community in the affiliated colleges of Anna University to learn and effectively teach the various concept of VLSI Design and to handle those concepts practically.

KEY TOPICS OF DISCUSSION

- ❖ CMOS Technology
- ❖ Circuit Characterization and Simulation
- ❖ Combinational Circuit Design
- ❖ Sequential Circuit Design
- ❖ CMOS Testing
- ❖ Specification using Verilog HDL

RESOURCE PERSONS

The sessions will be handled by experts from reputed institutions and Industries.

ELIGIBILITY

Faculty members of engineering colleges affiliated to Anna University. The total intake is 25 and selection will be based on first come first served basis.

LOCATION

The college is located at Karapakkam, about 8 kilometers from Adyar (South Chennai) on the OMR (IT Highway).

SEVEN DAYS FDTP

ON

VLSI DESIGN

01st Dec 2014 to 07th Dec 2014

REGISTRATION FORM

Coordinators

Dr. J. Raja Paul Perinbam

Professor/ECE

Mr. Jose Anand

Associate Professor/ECE



Sponsored by

**Anna University
Chennai**

Organized by

Department of
Electronics and Communication Engineering



KCG

COLLEGE OF TECHNOLOGY

KCG Nagar, Rajiv Gandhi Salai,
Karapakkam, Chennai – 600 097.

(Approved by AICTE, Affiliated to Anna
University, Accredited by NBA)

Ph: 044-2450 3232, 044-2450 3140

www.kcgcollege.ac.in

**Faculty Development Training Programme
On
VLSI DESIGN**

01st Dec 2014 to 07th Dec 2014

Name :
Gender : Male / Female
Qualification :
Designation :
Department :
Institution :

Address for Communication:

E-Mail :
Phone (Off) :
Mobile No.

Signature of the Applicant

REGISTRATION DETAILS

There is no registration fee for this programme. Selected candidates will be intimated by e-mail. No TA/DA will be paid. If selected, the applicant should attend the programme for the entire duration. Send the completed application form to the address before due date. Registration form given in the brochure or copy of this form can be used for registration.

DECLARATION

I declare that all the details furnished above are true to the best of my knowledge and I agree to abide by the rules and regulations governing the conduct of programme.

Date :

Place :

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Mr./Ms./Dr. is an employee of our institute and is hereby sponsored by us. He/She will be permitted to attend the course, if selected.

Signature with seal of Head of the
Institution / Organization

ADDRESS FOR CORRESPONDENCE

Mr. Jose Anand,
Associate Professor,
Department of ECE,
KCG College of Technology
KCG Nagar, Rajiv Gandhi Salai,
Karapakkam, Chennai -600 097,
e-mail: jose.anand.ece@kcgcollege.com
Mob: +91-9894280100

ORGANIZING COMMITTEE

CHIEF PATRON:

Prof. Dr. M. Rajaram,
Vice Chancellor
Anna University
Chennai

PATRON:

Dr. S. Ganesan
Registrar
Anna University
Chennai

CHAIR:

Dr. D. Mohan
Director, CFD

Dr. D. Sridharan
Addl. Director, CFD

CONVENORS:

Dr.T. RengaRaja
Principal
KCG College of Technology

Dr.Amos H. Jeeva Oli
Head of Department/ECE
KCG College of Technology

COORDINATORS:

Dr. J. Raja Paul Perinbam
Professor/ECE

Mr. Jose Anand
Associate Professor/ECE

VENUE:

Seminar Hall, ECE
KCG College of Technology

IMPORTANT DATES

Submission of Application	: 27.11.2014
Intimation of Selection	: 28.11.2014
Confirmation by Participant	: 28.11.2014